# BIGTREETECH TMC2130-V3.0



TMC2130 is a high-performance two-phase stepper motor driver chip with a standard SPI interface and a simple step/dir interface. Very low noise, high load driver does not lose step.

# Product features:

- 1. Up to 256 subdivisions;
- 2. Support SPI interface control;
- 3. Do not lose step when driving under high load;
- 4. Logic voltage: 3.3V/5V;
- 5. Input voltage range: 12V/24V DC;
- 6. Motor operating current 1.2a (peak 2A) can be adjusted

7. Spreadcycle ensures that the stepper motor has a smooth transition without dead zone current when passing zero.

TMC2130 stepper motor driver chip has almost no jitter;

8. StealthChop can drive the motor to work under extremely quiet sound, so it can be controlled

The noise of the motor is below 10dB, which is much lower than that of the traditional current controller

Type;

9. SpreadCycle PWM chopped mode is automatically divided between slow and fast attenuators

Setting up a hysteresis attenuation function, the average current reflects the configured normal current in sine

There will be no transition period at the zero crossing of the sine, which reduces the current and torque fluctuations that make the current waveform more similar to the sine wave. Compared with the traditional constant chopper mode of SpreadCycle PWM, the motor operates more smoothly and stably

#### Size parameters



#### **Parameter Description:**

Highest Resolution 256 microsteps per full step Step/Dir Interface with microstep interpolation microPlyer™ SPI Interface

**stealthChop™** for extremely quiet operation and smooth motion

**spreadCycle**<sup>™</sup> highly dynamic motor control chopper

**coolStep™** current control for energy savings up to 75%

Voltage Range 12V/24VDC

Passive Braking and freewheeling mode Full Protection & Diagnostics

# Working mode :

1.STEP/DIR mode:



- (1)SPI position welding  $0\Omega$  resistor, making the driver work in STEP/DIR mode.
- (2)Working mode selection: SDI (CFG1), SCK (CFG2)

Standalone Operation(SPI\_MODE=GND) STP/DIR MODE

CFG	CFG6/EN					
GND	GND -> Driver enable					
Vio -	Vio -> Driver disable					
Open	Open-> Driver enable with ramp down from 100% to 34% after about 3s					
CFG2 CFG1 Steps Interpolation Chopper Mode						
GND	GND	1	NO	spreadcycle		
GND	Vio	2	NO	spreadcycle		
GND	Open	2	Yes to 256	spreadcycle		
Vio	GND	4	NO	spreadcycle		
Vio	Vio	16	NO	spreadcycle		
Vio	Open	4	Yes to 256	spreadcycle		
Open	GND	16	Yes to 256	spreadcycle		
Open	Vio	4	Yes to 256	stealthchop		
Open	Open	16	Yes to 256	stealthchop		

2. SPI working mode wiring instructions:

Before wiring, it is necessary to select the hardware operation mode of the driver module:

(1)1. Solder CFG4 and CFG5 as shown in the purple area (CFG4 is connected to GND, CFG5 is connected to VCC); (Welding is correct to enable spreadcycle mode)

(2)Remove the resistor at the SPI position to put the driver in SPI mode



Wiring diagram is as follows:

#### **SPI Basics**

SPI MASTER	SPI SLAVE
SCK MOSI MISO CS/SS	SCK MOSI/SDI MISO/SDO CS/SS

Three lines common to all the devices: MOSI (Master Out Slave In) MISO (Master In Slave Out) SCK (Serial Clock) One line specific for every device: SS (Slave Select) / CS (Chip Select)

#### Wiring diagram:



3. Potentiometer adjustment instructions

Rotate the potentiometer clockwise: reduce Vref to reduce the driver current;

Turn the potentiometer counterclockwise: Increase Vref to increase the driver current.

The exact voltage of Vref must be measured when the main board is supplied with 12V or 24V.

Range of Vref values: default: 1V (±0.2); MAX: 2.4V; MIN: 0V;

Do not use too much force when rotating the potentiometer to prevent irreversible damage to the potentiometer; when it is rotated counterclockwise to the maximum value, it will become the minimum value if it continues to rotate; similarly, when it is rotated clockwise to the minimum value, If you continue to rotate, it will become the maximum value;

# SPI mode firmware change instructions:

3 E	ile <u>E</u> dit <u>S</u> election <u>V</u> iew <u>G</u> o <u>D</u> ebug <u>T</u> er	minal Help Configuration.h - Marlin-bugfix-2.0.x-SKR-V1.3-2130-12864 - Visual Studio Code [Administrator]			
n	EXPLORER	C Configuration.h × C pins_BIGTREE_SKR_V1.3.h			
<b>U'</b>	▲ OPEN EDITORS				
$\sim$	× C Configuration h Madin	616 * Stepper Drivers P 2130			
لر	C pins BIGTREE SKR V1 3 h Marlin\src\ni	617 *			
~ ~	4 MADIIN-BUGETY-2 0 Y-SKD-V1 3-2130-12864	618 * These settings allow Marin to tune stepper driver timing and enable advanced of the stepper driver timing advanced of the stepper driver time stepper d			
Ŷ		619 - Stepper univers that support them. You may also override timing options in com			
	C pins_SELENA_COMPACI.n	621 * A4988 is assumed for unspecified drivers.			
	C pins_SETHLh				
S	C pins_SILVER_GATE.h				
<b>12</b> -71	C pins_SMOOTHIEBOARD.h	624 * TMC2130, TMC2130_STANDALONE, TMC2208, TMC2208_STANDALONE,			
	C pins_STB_11.h				
	C pins_STEVAL.h	626 * TMC2160, TMC2160_STANDALONE, TMC5130, TMC5130_STANDALONE,			
ð	C pins_STM3R_MINLh	627 * IMC5160, IMC5160_STANDALONE			
•	C pins_STM32F1R.h	620 */[ 44966 , A5964 , DRV6625 , EV6729 , E0476 , IBC508 , IBC508 , IBC208			
	C pins_STM32F4.h	630 #define X DRIVER TYPE TMC2130			
	C pins_TEENSY2.h	631 #define Y_DRIVER_TYPE TMC2130			
	C pins_TEENSY31_32.h	632 #define Z_DRIVER_TYPE TMC2130			
	C pins_TEENSY35_36.h	633 //#define X2_DRIVER_TYPE A4988			
	C pins_TEENSYLU.h	634 //#define Y2_DRIVER_TYPE A4988			
	C pins_THE_BORG.h	635 //#define Z2_DRIVER_TYPE A4988			
	C pins_TRIGORILLA_13.h	030 //#detine 23_DRIVER_TYPE A4988			
	C pins_TRIGORILLA_14.h	637 #define to DKIVER_TYPE INC2130			
	C pins_ULTIMAIN_2.h	639 //#define E2 DRIVER TYPE A4988			
	C pins_ULTIMAKER_OLD.h	640 //#define E3_DRIVER_TYPE A4988			
	C pins_ULTIMAKER.h				
	C pins_ULTRATRONICS_PRO.h				
	C pins_VORON.h				
	C pins_ZRIB_V20.h	644 // Enable this feature if all enabled endstop pins are interrupt-capable.			
	C pins.h	645 // This will remove the need to poil the interrupt pins, saving many CPU cycles.			
	C pinsDebug_list.h	647			
	C pinsDebug.h	648 /**			
	C sensitive pins.h				
	► sd				
	G Marlin.cpp	651 * Enable if your probe or endstops falsely trigger due to noise.			
	C Marlin.h				
	C Configuration_adv.h	553 - Higher Values may affect repeatability of accuracy of some bed probes.			
	C Configuration.h	655 * This feature is not required for common micro-switches mounted on PCBs			
	M Makefile	656 * based on the Makerbot design, which already have the 100nF capacitor.			
	🕒 Marlin.ino				
	<ul> <li>aitianore</li> </ul>				
	/ travis.vml	660 //#define ENDSTOP_NOISE_THRESHOLD 2			
		601 662 //			
	⊊ platformio.ini				
	f) process-palette ison	PROBLEMS 2 OUTPUT DEBUG CONSOLE TERMINAL			
	README.md				
- 10	▶ OUTLINE	C:\Users\win7\Deskton\Marlin-bugfix-2.0.x-5KR-V1.3-2130-12864>			
⊗ 0 ▲	©0≜2 ☆ ✓ + Ф ≧ ≜ B Æ 国				

For example, (Marlin-2.0)BIGTREETECH SKR V1.3 : Configuration.h file:

# Configuration\_adv.h file:

🛿 File Edit Selection View Go Debug Terminal Help Configuration_adv.h - Marlin-bugfix-2.0.x-SKR-V1.3-2130-12864 - Visual Studio Code [Administrator]					
n)	EXPLORER	C Configuration.h	C Configuration_adv.h ×		
<b>.</b>	▲ OPEN EDITORS	1395 */	0		▶ 2130
Ω	C Configuration.h Marlin	1396 #if HAS_	FRINAMIC		
-	× C Configuration_adv.h Marlin				
v	MARLIN-BUGFIX-2.0.X-SKR-V1.3-2130-12864	1398 #define	PHOLD_MULTIPLIER 0.5	<pre>// Scales down the holding current from // Interpolate X/X/7 MICROSTERS to 25</pre>	m run current
8	C pins_SELENA_COMPACT.h	1400			
æ	C pins_SETHLh	1401 #if AX	IS_IS_TMC(X)		
S	C pins_SILVER_GATE.h	1402 #def:	ine X_CURRENT 800 //	(mA) RMS current. Multiply by 1.414 for	
63	C pins_SMOOTHIEBOARD.h	1403 #def: 1404 #def	INE X_MICRUSTEPS 16 //	0256	
	C pins_STB_ILIN	1405 #endif			
<u></u>					
¥	C pins_STM32F1R.h	1407 #if AX	IS_IS_TMC(X2)		
	C pins_STM32F4.h		ine X2_CORRENT 800		
	C pins_TEENSY2.h		ine X2_RSENSE 0.11		
	C pins_TEENSY31_32.h	1411 #endif			
	C pins_TEENSY35_36.h	1412 1412 #if AV			
	C pins_TEENSYLU.h	1415 #11 AX	ine Y CURRENT 800		
	C pins_THE_BORG.h	1415 #def:	ine Y_MICROSTEPS 16		
	C pins_TRIGORILLA_13.h	1416 #def:	ine Y_RSENSE 0.11		
	C pins_TRIGORILLA_14.h	1417 #endit			
		1419 #if AX	IS_IS_TMC(Y2)		
	C pins ULTIMAKER.h	1420 #def:	ine Y2_CURRENT 800		
	C pins_ULTRATRONICS_PRO.h	1421 #def:	ine Y2_MICROSTEPS 16		
	C pins_VORON.h	1422 #det: 1423 #endif	LINE YZ_KSENSE 0.11		
	C pins_ZRIB_V20.h	1424			
	C pins.h	1425 #if AX	IS_IS_TMC(Z)		
	C pinsDebug_list.h	1426 #def:	ine Z_CURRENT 800		
	C pinsDebug.h	1427 #def:	ine Z_MICROSTEPS 16		
	C sensitive pins.h	1428 #def:	ine Z_RSENSE 0.11		
	A set	1429 #endif			
	P SU	1430			
	e Marin.cpp	1431 #if AX	IS_IS_TMC(Z2)		
	C Marlin.h	1432 #def:	ine Z2_CURRENT 800		
	C Configuration_adv.h	1433 #det:	LINE Z2_MICROSTEPS 16		
	C Configuration.h	1434   #det:	LNE ZZ_KSENSE 0.11		
	M Makefile	1435 #enult			
	🕒 Marlin.ino	1430			
	▲ aitattributoc	143/ #1† AX.	I2_I2_IMC(23)		
		1438 #det:	LNE Z3_CURRENT 800		
	v .gitignore	1439 #def:	Ine Z3_MICROSTEPS 16		
	! .travis.yml	1440   #def:	ine Z3_RSENSE 0.11		
	🕺 LICENSE	1441 #endif			
	≣ platformio.ini	1442 1442 #if AV			
	() process-palette.json	1445 #1T AX.	IS_IS_INC(E0)		
*	(i) README.md	1445 #def	ine EO_COMICHT 800		
1	♦ OUTLINE	1446 #def	ine EØ RSENSE 0.11		
80	2 🕼 🗸 🛊 🕸 📋 🛓 🖡 🗵				(Global



### pins\_BIGTREE\_SKR\_V1.3.h file:

1	File Edit Selection View Go Debug Ter	minal	Help	pins_BIGTREE_SKR_V	1.3.h - Marlir	n-bugfix-2.0.x-SKR-V1.3-2130-12864 - Visi
a	EXPLORER	C Co	nfiguration.h	C pins_BIGTREE_	SKR_V1.3.h	×
יש	A OPEN EDITORS	57	#define	X_STEP_PIN	P2_02	
0	C Configuration h Marlin		#define	X_DIR_PIN	P2_06	
2	× C pins RIGTREE SKR V1.3.h Martin\sr\ai		#define	X_ENABLE_PIN	P2_01	
00	MARLIN-BUGFIX-2.0.X-SKR-V1.3-2130-12864	60	#ifndef	X_CS_PIN	101 472	
Ŷ	C pins ARCHIM2.h	61 62	#defin #ondif	e X_CS_PIN	P1_1/	
	C pins ARMED.h		HEIMAN			
	C pins AZSMZ MINLh		#define	Y_STEP_PIN	P0_19	
S	C pins AZTEFG X1.h		#define	Y_DIR_PIN	P8_20	
627	C pips AZTEEG X3 PRO.h		#define	Y_ENABLE_PIN	P2_08	
121	C pins AZTEEG X3.h	67	#itndet	Y_CS_PIN		
1.1	C pips A7TEFG X5 GTh	68	#defin	e Y_CS_PIN	P1_15	
Ð	C pins AZTEEG X5 MINI WIFLb		#endit			
	C pins BAM DICE DUE h	71	#define	Z STEP PIN	PØ 22	
	C pins BFAST.h	72	#define	Z_DIR_PIN	P2_11	
	C pins BIGTREE SKR V1.1 h		#define	Z_ENABLE_PIN	P0_21	
	C pins BIGTREE SKR V13h		#ifndef	Z_CS_PIN		
	C pins BIOLI B300 V1 0.h		#defin	e Z_CS_PIN	P1_10	]
	C pins BIOU BO111 A4h	/6 77	#endit			
	C pins BIOU KEB 2h	78	#define	EØ STEP PIN	P2 13	
	C pins BO ZUM MEGA 3D.h		#define	E0_DIR_PIN	PØ 11	
	C pips BRAINWAVE PRO.b		#define	EØ ENABLE PIN	P2 12	-
	C pins BRAINWAVE h	81	#ifndef	E0_CS_PIN		
	C pins CHEAPTRONIC.h	82	#defin	e E0_CS_PIN	P1_08	
	C pins CHEAPTRONICv2.h	83	#end17			
	C pins CHITU3D.h	85	#define	E1 STEP PIN	PØ 01	
	C pins CNCONTROLS 11.h		#define	E1_DIR_PIN	P0 00	
	C pins CNCONTROLS 12.h	87	#define	E1_ENABLE_PIN	P0 10	2
	C pins COHESION3D MINLh		#ifndef	E1_CS_PIN		
	C pins COHESION3D REMIX.h	89	#defin	e E1_CS_PIN	P1_01	<u>_</u>
	C pins DUE3DOM MINLh		#end1†			
	C pins DUE3DOM.h	92				
	C pins DUPLICATOR 13 PLUS.h		#ifndef	FIL_RUNOUT_PIN		
	C pins EINSTART-S.h	94	#defin	e FIL_RUNOUT_PIN	P1_28	
	C pins_EINSY_RAMBO.h		#endif			
	C pins_EINSY_RETRO.h	96				
	C pins ELEFU 3.h	97	11 Softw	and SDT nine for	TMCD120 c	tannan drivans
	C pins_ESP32.h		// 30110	are set pros (or	110-2130 3	cepper distoris
	C pins_FELIX2.h		#if ENAB	LED(TMC_USE_SW_SP	I)	
	C pins_FORMBOT_RAPTOR.h		#defin	e TMC_SW_MOSI	P4_28	
	C pins_FORMBOT_RAPTOR2.h	102	#defin	e TMC_SW_MISO	P0_05	
	C pins_FORMBOT_TREX2PLUS.h	103	#defin	e TMC_SW_SCK	P0_04	
	C pins_FORMBOT_TREX3.h	104	#end1f			
	C pins_FYSETC_F6_13.h	PROB	lems 🗿 o	UTPUT DEBUG CONSOLI	E TERMINA	AL.
-	C pins_GEN3_MONOLITHIC.h					
- 10	▶ OUTLINE	C:\U	sers\win7\De	sktop\Marlin-bu∉fi>	-2.0.x-SK	R-V1.3-2130-12864>
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After firmware debugging, compile and upload to the motherboard, open <u>e pronterface</u> online Print software, connected to the motherboard can view the SPI mode 2130 drive Dynamic running state.

#### Note:

- When selecting SPI operation mode for hardware, please use soldering iron carefully to avoid burns. After processing, observe carefully whether there is residual tin residue in the module, and clean it up to prevent short-circuit and burning of the module;
- (2) Please pay attention to the wire sequence and IO port when wiring. If the wrong wire is connected, the drive will not work. Connect them as shown above carefully.
- (3) When the driver is inserted into the motherboard, please pay attention to see the direction of the drive, can not be inserted in reverse, to prevent the drive from burning;
- (4) Be sure to do a good job before driving heat dissipation work (heat sink + fan), to prevent the drive is not working properly

If you have problems in use, welcome to contact us, we will be careful to answer for you;

If you have any good comments or suggestions on our products, please feel free to give us your feedback.

We will also carefully consider your comments or Suggestions, thank you for choosing BIGTREETECH Product, thank you!